Notice of References Cited

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_	Application/Control No.	pplicant(s)/Pater	nt Under
	09/678,175	ਜੇeexamination KONRAD ET AL.	
	Examiner	Art Unit	
	Helen B Rossoshek	2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,311,079	05-1994	Ditlow et al.	326/39
	В	US-6,492,835	12-2002	Shau	326/41
	С	US-4,839,539	06-1989	Takata et al.	326/38
	D	US-6,442,732	08-2002	Abramovici et al.	716/4
	Е	US-6,433,577	08-2002	Wang et al.	326/37
	F	US-6,038,386	03-2000	Jain	716/16
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			·

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					·
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Katkoori et al., "Simulation based architectural power estimation for PLA-based controllers", Aug. 1996, Low Power Electronics and Design, 1996, International Symposium on, 12-14, Pages 121-124.
	V	Khatri et al., "Cross-talk immune VLSI design using a network of PLAs embedded in a regular layout fabric", Nov. 2000, Computer Aided Design, 2000. ICCAD-2000, IEEE/ACM International Conference on, Pages: 412-418.
	w	
	×	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.